COMPILATION ARCHITECTURE

SEVENTH FRAMEWORK
PROGRAMME

The Steering Committee of the
HiPEAC Network of Excellence
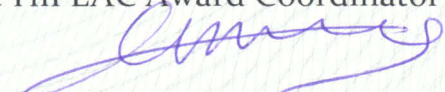has awarded a

HiPEAC Paper Award

for the paper

*Towards variation-aware system-level power estimation of DRAMs:
an empirical approach*

by Karthik Chandrasekar, Christian Weis, Benny Akesson, Norbert Wehn,
Kees Goossens

at the **Design Automation Conference**

The HiPEAC Award Coordinator

Prof. Koen De Bosschere